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# FastSpec: Scalable Generation and Detection of Spectre Gadgets Using Neural Embeddings

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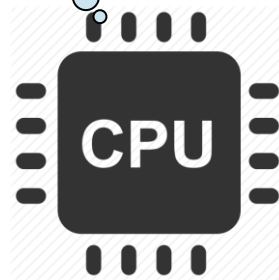
<sup>2</sup> Iowa State University

# Speculative Execution

- array1\_size not cached

I don't know **array1\_size** yet.  
I will execute the next line.

```
1 void user_function_v01(size_t x) {  
2     if (x < array1_size) {  
3         temp &= array2[array1[x] * 512];  
4     }  
5 }
```

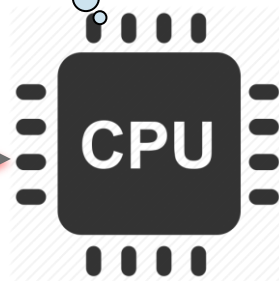


# Spectre Attack (v1)

- Kocher et al, *Spectre Attacks: Exploiting Speculative Execution*, S&P '19
- Mitigation: Software Updates

I don't know **array1\_size** yet.  
I will execute the next line.

```
1 void user_function_v01(size_t x) {  
2     if (x < array1_size) {  
3         temp &= array2[array1[x] * 512];  
4     }  
5 }
```



# Problem 1: What do they look like?

```
declspec(noinline) void leakByteNoinlineFunction(uint8_t k) { temp &= array2[(k) * 512]; }
void victim_function_v03(size_t x) {
    if (x < array1_size)
        leakByteNoinlineFunction(array1[x]);
}

inline int is_x_safe(size_t x) { if (x < array1_size) return 1; return 0; }
void victim_function_v13(size_t x) {
    if (is_x_safe(x))
        temp &= array2[array1[x] * 512];
}

void victim_function_v04(size_t x) {
    if (x < array1_size)
        temp &= array2[array1[x] * 512];
}

void victim_function_v15(size_t x) {
    if (x < array1_size)
        temp &= array2[array1[x] * 512];
}

void victim_function_v07(size_t x) {
    static size_t last_x = 0;
    if (x == last_x)
        temp &= array2[array1[x] * 512];
    if (x < array1_size)
        last_x = x;
}

void victim_function_v11(size_t x) {
    if (x < array1_size)
        temp = memcmp(&temp, array2 + (array1[x] * 512), 1);
}

void leakByteLocalFunction_v02(uint8_t k) { temp &= array2[(k) * 512]; }
void victim_function_v02(size_t x) {
    if (x < array1_size)
        leakByteLocalFunction_v02(array1[x]);
}

void victim_function_v08(size_t x) {
    temp &= array2[array1[x < array1_size ? (x + 1) : 0] * 512];
}

void victim_function_v14(size_t x) {
    if (x < array1_size)
        temp &= array2[array1[x ^ 255] * 512];
}

void victim_function_v12(size_t x, size_t y) {
    if ((x + y) < array1_size)
        temp &= array2[array1[x + y] * 512];
}

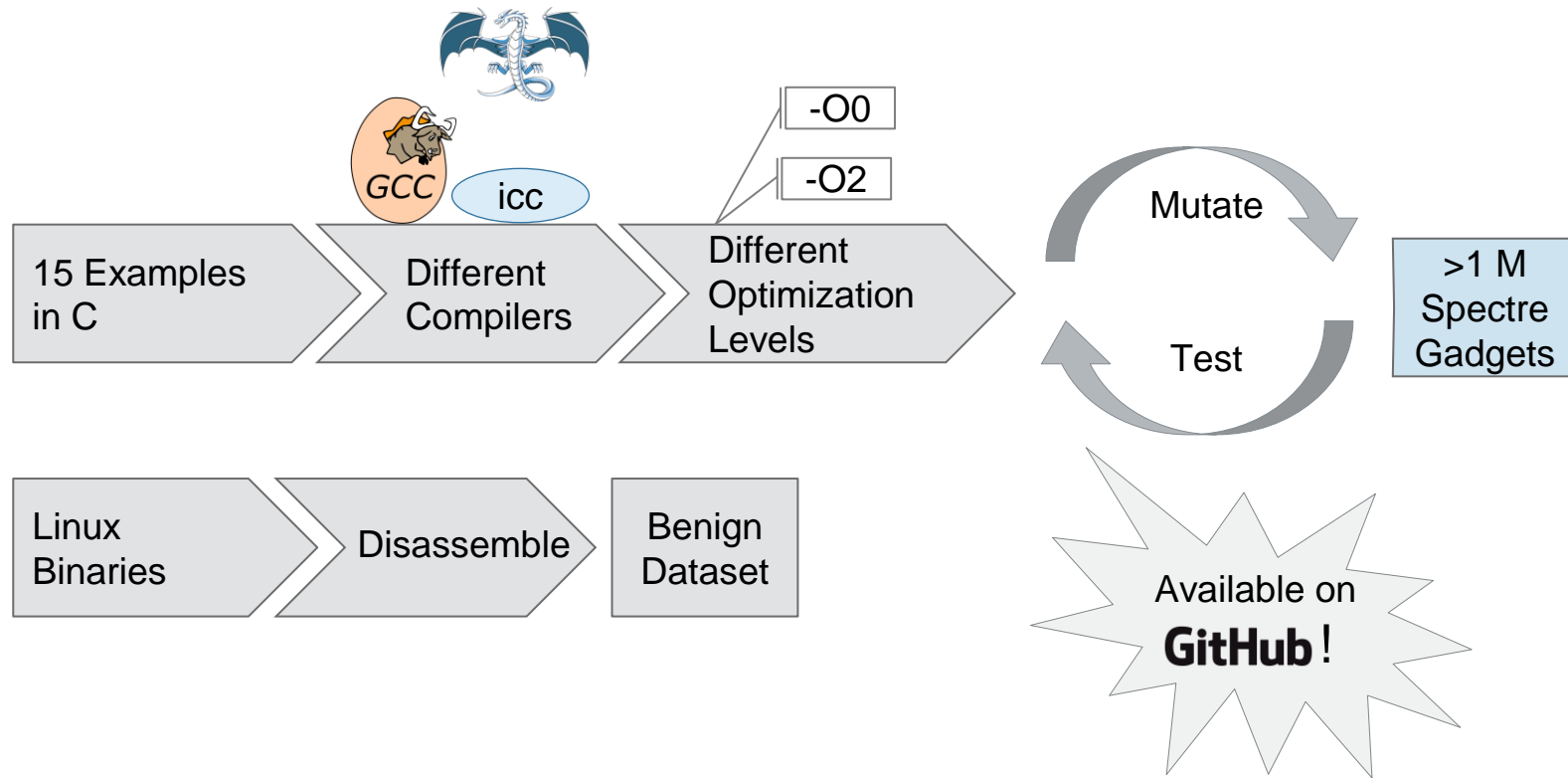
void victim_function_v05(size_t x) {
    size_t i;
    if (x < array1_size)
        for (i = x - 1; i >= 0; i--)
            temp &= array2[array1[i] * 512];
}

void victim_function_v10(size_t x, uint8_t k) {
    if (x < array1_size) {
        if (array1[x] == k)
            temp &= array2[0];
    }
}

void victim_function_v06(size_t x) {
    if ((x & array_size_mask) == x)
        temp &= array2[array1[x] * 512];
}

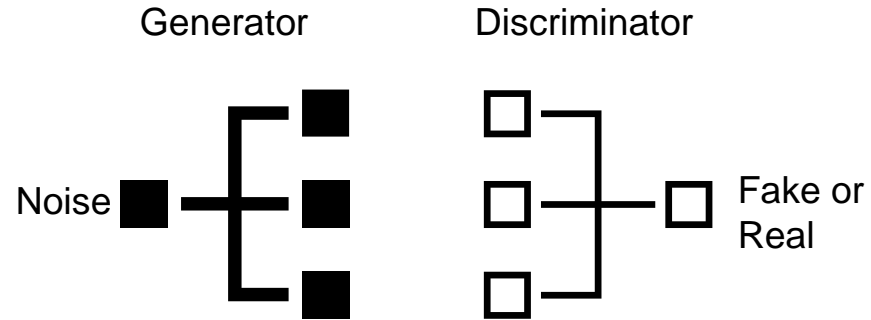
void victim_function_v09(size_t x, int *x_is_safe) {
    if (*x_is_safe)
        temp &= array2[array1[x] * 512];
}
```

# Creating Spectre Gadget Dataset in Assembly



# SpectreGAN

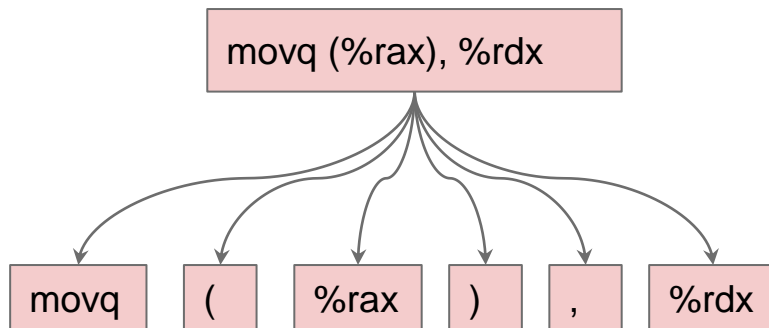
- Spectre gadget generator using Generative Adversarial Networks (Goodfellow et al, NIPS14)
- MaskGAN (Fedus et al, ICLR18)



# Tokenization

---

- <imm> Immediate
- <label> Label
- <UNK> Unknown label



# SpectreGAN

---

## Input Gadget

```
pushq %rbp
movq %rsp , %rbp
movb %sil , %al
movq %rdi , -8 ( %rbp )
movb %al , -9 ( %rbp )
movq -8 ( %rbp ) , %rdi
:
```

## Generator

## Discriminator

1



# SpectreGAN

---

## Input Gadget

```
pushq %rbp
movq %rsp , %rbp
movb %sil , %al
movq %rdi , -8 ( %rbp )
movb %al , -9 ( %rbp )
movq -8 ( %rbp ) , %rdi
:
```

## Generator

pushq %rbp <MASK><MASK><MASK> %rbp

## Discriminator

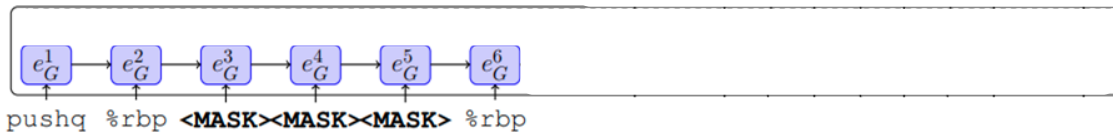
⌂

# SpectreGAN

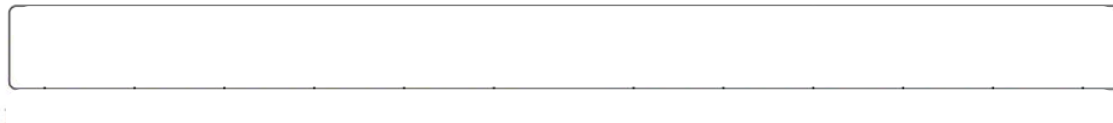
## Input Gadget

```
pushq %rbp
movq %rsp , %rbp
movb %sil , %al
movq %rdi , -8 ( %rbp )
movb %al , -9 ( %rbp )
movq -8 ( %rbp ) , %rdi
:
```

## Generator



## Discriminator

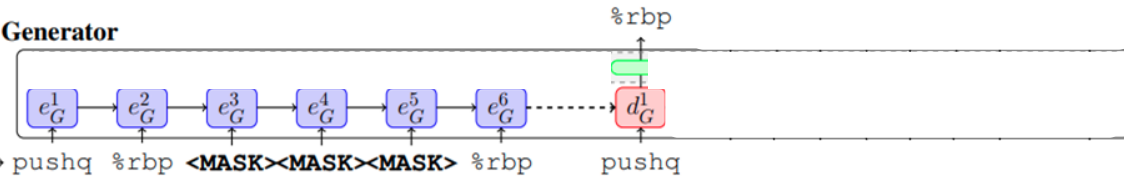


# SpectreGAN

## Input Gadget

```
pushq %rbp
movq %rsp , %rbp
movb %sil , %al
movq %rdi , -8 ( %rbp )
movb %al , -9 ( %rbp )
movq -8 ( %rbp ) , %rdi
:
```

## Generator



## Discriminator

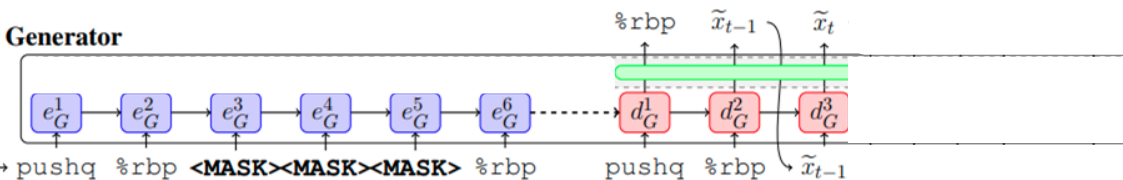


# SpectreGAN

### Input Gadget

```
pushq %rbp
movq %rsp, %rbp
movb %sil, %al
movq %rdi, -8(%rbp)
movb %al, -9(%rbp)
movq -8(%rbp), %rdi
⋮
```

## Generator



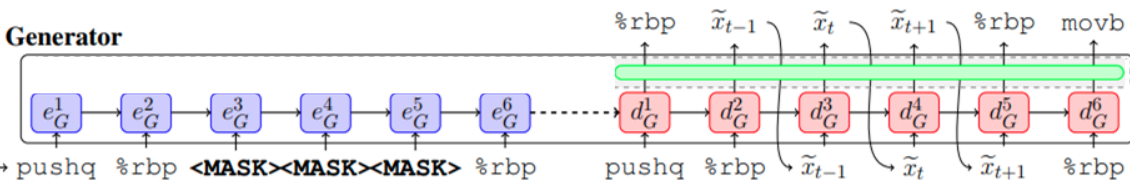
## Discriminator

# SpectreGAN

## Input Gadget

```
pushq %rbp
movq %rsp , %rbp
movb %sil , %al
movq %rdi , -8 ( %rbp )
movb %al , -9 ( %rbp )
movq -8 ( %rbp ) , %rdi
:
```

## Generator



## Discriminator

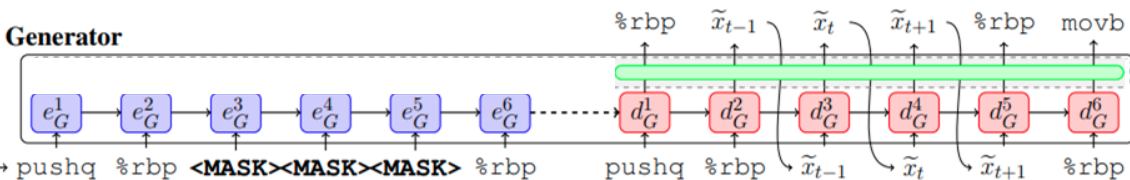


# SpectreGAN

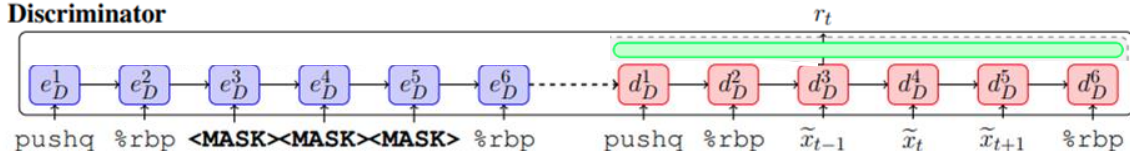
## Input Gadget

```
pushq %rbp
movq %rsp, %rbp
movb %sil, %al
movq %rdi, -8(%rbp)
movb %al, -9(%rbp)
movq -8(%rbp), %rdi
:
```

## Generator



## Discriminator



# SpectreGAN

- ~3 days of training
- Assembly function syntax without any supervision
- 70% success rate in the compiled samples compare to 5% in fuzzing.

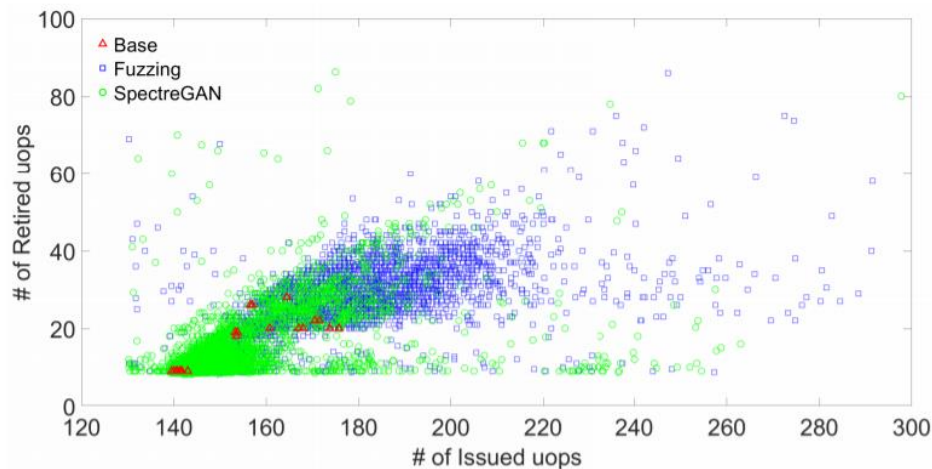
```
1 victim_function:
2 .cfi_startproc
3 movl size(%rip),%eax
4 cmpq %rdi,%rax
5 jbe .L0
6 leaq array1(%rip),%rax
7 movzbl (%rdi,%rax),%eax
8 ror $1,%rsi
9 shlq $9,%rax
10 leaq array2(%rip),%rcx
11 movss %xmm8,%xmm4
12 movb (%rax,%rcx),%al
13 andb %al,temp(%rip)
14 movd %xmm1,%r14d
15 test %r15,%rcx
16 sbbbl %r13d,%r9d
17 .L0:
18 retq
19 cmovll %r8d,%r10d
20 .cfi_endproc
```



```
1 victim_function:
2 .cfi_startproc
3 movl size(%rip),%eax
4 cmpq %rdi,%rax
5 jbe .L0
6 leaq array1(%rip),%rax
7 movzbl (%rdi,%rax),%eax
8 ror $1,%rsi
9 shlq $9,%rax
10 movb array2(%rdi),%al
11 andb %al,temp(%rip)
12 .L1:
13 andb %r13b,%al
14 movb array2(%rax),%al
15 andb %al,temp(%rip)
16 sbbbl %r13d,%r9d
17 .L0:
18 retq
19 cmovll %r8d,%r10d
20 .cfi_endproc
```

# SpectreGAN

- Unique n-gram analysis (n=5)
  - Base 4.7K
  - Fuzzing ~1M
  - SpectreGAN ~1M
  - ~2M in total
- Microarchitectural analysis
  - uops\_issued vs uops\_retired
- Detection analysis
  - oo7 and Spectector tools





## Problem 2: Where are they?

---

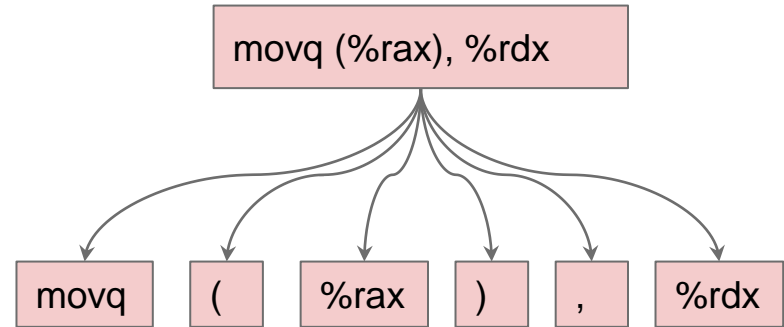
- We can blindly put Ifence after every branch.
  - With 62-74.8% performance overhead (Carruth, 2018)

Or...

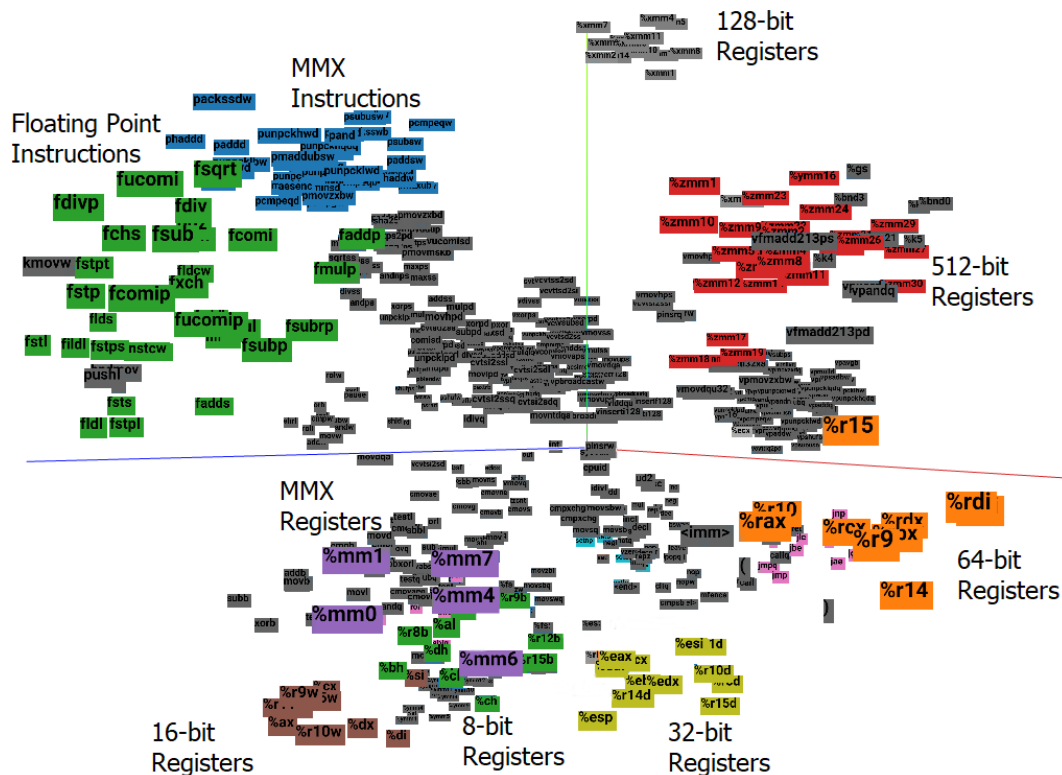
- We can build a \_\_\_\_\_ tool to find the Spectre gadgets.
  - automated
  - scalable
  - accurate

# FastSpec

- BERT-based vulnerability detector  
(Devlin et al, NAACL18)
- Scans binaries with linear complexity

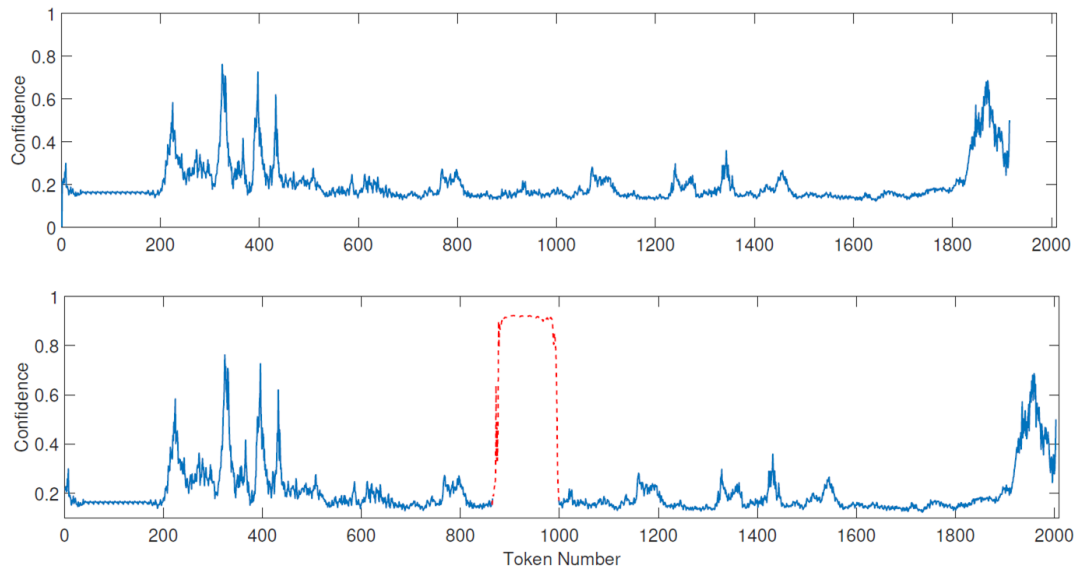


- Vector representations
- t-SNE visualization



# FastSpec

```
push r12 push rbp push rax callq <label> %rax, %rax je <label> lea <imm> ( %rax ), %rbx lea <imm>
%r12 push %rbp push %rbx callq <label> %rax, %rax je <label> lea <imm> ( %rax ), %rbx lea <imm> ( %r
push %rbx callq <label> %rax, %rax je <label> lea <imm> ( %rax ), %rbx lea <imm> ( %rax ), %rbp
%rbx callq <label> %rax, %rax je <label> lea <imm> ( %rax ), %rbx lea <imm> ( %rax ), %rbp mov
callq <label> %rax, %rax je <label> lea <imm> ( %rax ), %rbx lea <imm> ( %rax ), %rbp mov %rax
<label> %rax, %rax je <label> lea <imm> ( %rax ), %rbx lea <imm> ( %rax ), %rbp mov %rax, %r12
%rax, %rax je <label> lea <imm> ( %rax ), %rbx lea <imm> ( %rax ), %rbp mov %rax, %r12 nopl <
, %rax je <label> lea <imm> ( %rax ), %rbx lea <imm> ( %rax ), %rbp mov %rax, %r12 nopl <imm>
%rax je <label> lea <imm> ( %rax ), %rbx lea <imm> ( %rax ), %rbp mov %rax, %r12 nopl <imm> ( %
je <label> lea <imm> ( %rax ), %rbx lea <imm> ( %rax ), %rbp mov %rax, %r12 nopl <imm> ( %rax
<label> lea <imm> ( %rax ), %rbx lea <imm> ( %rax ), %rbp mov %rax, %r12 nopl <imm> ( %rax ) no
le <imm> ( %rax ), %rbx lea <imm> ( %rax ), %rbp mov %rax, %r12 nopl <imm> ( %rax ) mov ( %rbx
<imm> ( %rax ), %rbx lea <imm> ( %rax ), %rbp mov %rax, %r12 nopl <imm> ( %rax ) mov ( %rbx ),
( %rax ), %rbx lea <imm> ( %rax ), %rbp mov %rax, %r12 nopl <imm> ( %rax ) mov ( %rbx ), %rdi
%rax ), %rbx lea <imm> ( %rax ), %rbp mov %rax, %r12 nopl <imm> ( %rax ) mov ( %rbx ), %rdi le
), %rbx lea <imm> ( %rax ), %rbp mov %rax, %r12 nopl <imm> ( %rax ) mov ( %rbx ), %rdi lea <im
, %rbx lea <imm> ( %rax ), %rbp mov %rax, %r12 nopl <imm> ( %rax ) mov ( %rbx ), %rdi lea <imm>
%rbx lea <imm> ( %rax ), %rbp mov %rax, %r12 nopl <imm> ( %rax ) mov ( %rbx ), %rdi lea <imm>
lea <imm> ( %rax ), %rbp mov %rax, %r12 nopl <imm> ( %rax ) mov ( %rbx ), %rdi lea <imm> ( %rip
<imm> ( %rax ), %rbp mov %rax, %r12 nopl <imm> ( %rax ) mov ( %rbx ), %rdi lea <imm> ( %rip ),
( %rax ), %rbp mov %rax, %r12 nopl <imm> ( %rax ) mov ( %rbx ), %rdi lea <imm> ( %rip ), %r12
%rax ), %rbp mov %rax, %r12 nopl <imm> ( %rax ) mov ( %rbx ), %rdi lea <imm> ( %rip ), %r12 ad
), %rbp mov %rax, %r12 nopl <imm> ( %rax ) mov ( %rbx ), %rdi lea <imm> ( %rip ), %r12 add <im
%rbp mov %rax, %r12 nopl <imm> ( %rax ) mov ( %rbx ), %rdi lea <imm> ( %rip ), %r12 add <imm>
%rbp mov %rax, %r12 nopl <imm> ( %rax ) mov ( %rbx ), %rdi lea <imm> ( %rip ), %r12 add <imm>, %rbx
%rax, %r12 nopl <imm> ( %rax ) mov ( %rbx ), %rdi lea <imm> ( %rip ), %r12 add <imm>, %rbx
%r12 nopl <imm> ( %rax ) mov ( %rbx ), %rdi lea <imm> ( %rip ), %r12 add <imm>, %rbx callq <lab
%rax nopl <imm> ( %rax ) mov ( %rbx ), %rdi lea <imm> ( %rip ), %r12 add <imm>, %rbx callq <lab
```



# Case Study 1: OpenSSL

- OpenSSL v3 “*speed*” benchmark
- SpecFuzz (Oleksenko et al, USENIX '20)
- Sliding window of size 80 tokens
- AUC=0.998
- FP=0.04%, FN=2%

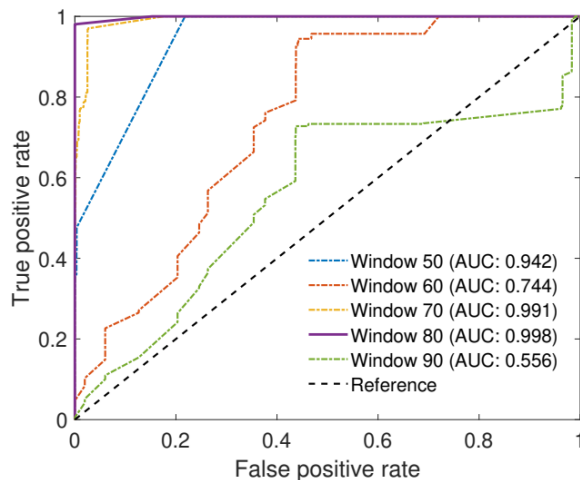
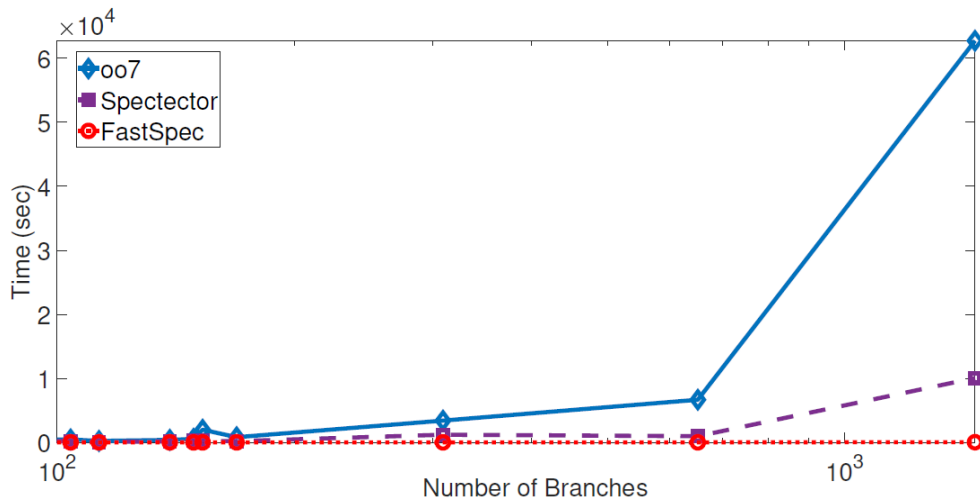


Figure 5: Solid line stands for the ROC curve of Fast-Spec for Spectre gadget class. Dashed line represents the reference line.

## Case Study 2: Phoronix Test Suite

- State of the art tools are not scalable.
- Crafty benchmark
  - 10K branches
  - 0.6 MB
- Spectector: 2 days
- oo7: 10+ days
- FastSpec: **<6 mins**



# Case Study 2: Phoronix Test Suite

TABLE 2: Comparison of *oo7* [6], Spectector [8], and FastSpec on the Phoronix Test Suite. The last column shows that FastSpec is on average 455 times faster than *oo7* and 75 times faster than *Spectector*. (#CB: Number of conditional branches, #Fc: Number of functions, #DFc: Number of detected functions)

Benchmark	Size (KB)	#CB	#Fc	SpecFuzz	oo7			Spectector			FastSpec		
				#DFc	Precision	Recall	Time (sec)	Precision	Recall	Time (sec)	Precision	Recall	Time (sec)
Byte	183.5	363	83	7	0.70	<b>0.90</b>	400	1.00	0.43	115	<b>1.00</b>	0.86	<b>14</b>
Clomp	79.4	1464	45	1	0	0	17.5 hr	0.05	0.9	2.8 hr	<b>1.00</b>	<b>1.00</b>	<b>35</b>
Crafty	594.8	10796	207	44	<b>1.00</b>	0.54	>10 day	0.60	<b>0.91</b>	48 hr	0.23	0.80	<b>315</b>
C-ray	27.2	139	11	1	<b>1.00</b>	1.00	395	0.2	0.9	153	0.50	<b>1.00</b>	<b>8</b>
Ebizzy	18.5	104	6	3	0	0	467	0.60	1.00	206	<b>1.00</b>	0.33	<b>3</b>
Mbw	13.2	70	5	1	0	0	145	<b>0.50</b>	1.00	34	0.33	<b>1.00</b>	<b>2</b>
M-queens	13.4	51	4	1	1.00	1.00	136	0.50	1.00	24	<b>1.00</b>	<b>1.00</b>	<b>2</b>
Postmark	38.0	309	49	6	1.00	0.83	3409	0.43	0.95	1202	<b>1.00</b>	<b>1.00</b>	<b>10</b>
Stream	22.0	113	4	3	0	0	231	0	0	63	<b>1.00</b>	<b>0.66</b>	<b>4</b>
Tiobench	36.1	169	19	1	0	0	813	0.25	0.8	201	<b>0.33</b>	<b>1.00</b>	<b>9</b>
Tscp	40.8	651	38	13	0	0	6667	1.00	0.15	972	<b>1.00</b>	<b>0.92</b>	<b>12</b>
Xsbench	27.9	153	32	1	<b>1.00</b>	<b>1.00</b>	1985	0	0	249	0.50	0.90	<b>7</b>
Average					0.47	0.44		0.43	0.67		<b>0.74</b>	<b>0.87</b>	

---



# Conclusion

- New Spectre gadget dataset with 1+ million samples
- Task specific assembly code generation
- New DL-based Spectre v1 detection tool



Reproduce results:



**vernamlab/FastSpec**

Contact me:



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**canertol**